AICTE TRAINING & LEARNING ACADEMY (ATAL)
Sponsored
6 Days Online
Faculty Development Program
On
"Future Tech Revealed: Semiconductors, Quantum Computing, and Data Storage Insights"

January 20 to 25, 2025

Organized by
Department of Electronics and Telecommunication
COEP Technological University, Pune
Wellesley Rd, Shivajinagar, Pune, Maharashtra 411005

Registration is free

Registration

The FDP is open to the faculty members of AICTE/UGC approved Engineering Institutes/ Universities/ R&D Labs/ Industry Persons/ PhD Scholars/ postgraduates interested to know about the current status, scope and challenges about This course offers a deep dive into the fundamentals of Silicon technology and memory storage devices, providing you with a solid foundation in how data is encoded, stored, and retrieved.

No registration fee will be collected from the participants

All interested participants should register on ATAL portal only.

https://atalacademy.aicte-india.org/login

Application No.: 1730702582

Title: "Future Tech Revealed: Semiconductors, Quantum Computing, and Data Storage Insights"

Mode: Online,
Date: 20th to 25th January 2025 (6:00pm onwards)

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Prelude
This course offers a deep dive into the fundamentals of Silicon technology and memory storage devices, providing you with a solid foundation in how data is encoded, stored, and retrieved. You'll explore key principles of digital storage technology and gain hands-on experience in designing efficient storage solutions with coding exercises. The course also introduces next-generation quantum devices, giving you a glimpse into the future of storage technology. Whether you are a technology enthusiast or a professional looking to enhance your skills, this course will equip you with the knowledge and practical skills to navigate and innovate in the rapidly evolving field of digital storage. Prepare to unlock the full potential of modern storage technologies and stay ahead of industry trends.

Objectives of the course

Learn the Modern Storage Solutions	Develop coding skills	Gain Knowledge of Storage Designs
Pre-Silicon Verification Strategies	Precision Engineering	Clarify your doubts

Outcomes of the FDP
Participants will demonstrate the ability to
- Explain fundamentals of Silicon technology and memory storage devices
- Describe the key principles of digital storage technology, including data encoding, storage mediums, and data retrieval processes.
- Design Efficient Storage Solutions with Code.
- Understand the Next Generation Quantum Devices

Register now Seats are limited

Registration is free

RESOURCE PERSONS

Rita Gupta
Fellow at AMD

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Senior director, Cadence

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Research scientist Intel

Shekhar Nilangekar
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