

Department of Electronics and Telecommunication Engineering

COEP Technological University

(A Unitary Public University of Government of Maharashtra)

Time Table

Class : **T. Y. B. Tech E & TC**
With Effect From: 17.08.2023

Academic Year : 2023 - 2024
Term : I (First)

Day	9.00 am To 10.00 am	10.00 am To 11.00 am	11.00 am To 12.00 pm	12.00 pm To 1.00 pm	1.00 pm To 2.00 pm	2.00 pm To 3.00 pm	3.00 pm To 4.00 pm	4.00 pm To 5.00 pm	5.00 pm To 6.00 pm
MON	CLPD Tut (A) ET-003	PSE (RM) ET-003	EMWA (AGA) ET-003	Lunch Break	DSP(A), DC(B), CLPD(C), DA(D), RPP(E)		DC (VNM) ET-003	CLPD Tut (B), ET-003	Minor(PPT)- SH-001/ Honors (SMT)- ET-003
TUE	CLPD Tut (C) ET-003	PSE (RM) ET-003	EMWA (AGA) ET-003		DSP(B), DC(C), CLPD(D), DA(E), RPP(A)		DSP (NRK) ET-003	DC (VNM) ET-003	Minor(PPT)- SH-001/ Honors (SMT)- ET-003
WED	CLPD Tut (D) ET-003	PSE - Tut (RM) ET-003	DSP (NRK) ET-003		DSP(C), DC(D), CLPD(E), DA(A), RPP(B)		IFC - DA () ET-003	CLPD Tut (E), ET-003	Minor(PPT)- SH-001/ Honors (SMT)- ET-003
THU	COI () ET-003	EMWA (AGA) ET-003	DSP (NRK) ET-003		DSP(D), DC(E), CLPD(A), DA(B), RPP(C)		CLPD (VVI) ET-003	HSSOC (Humanities)	
FRI	EPP () ET-003	CLPD (VVI) ET-003	CLPD (VVI) ET-003		DSP(E), DC(A), CLPD(B), DA(C), RPP(D)		DC (VNM) ET-003	HSSOC (Humanities)	
SAT									

Configurable Logic and Processor Design (CLPD) (Th + Lab Batches - A & B): Dr. Mrs. V. V. Ingale, **(CLPD) (Lab Batches - C, D & E):** Dr. Mrs. V. S. Agarwal,
Digital Signal Processing (DSP) (Th + Lab): Ms. N. R. Kolhare, **Digital Communication (DC) (Th + Lab):** Dr. Mrs. V. N. More,
Electromagnetic Waves and Antennas (EMWA) (Th): Mrs. A. G. Andurkar, **R and Python Programming (Lab):** Mr. R. V. Sadakale,
Configurable Logic and Processor Design (CLPD) (Tut): **Probability and Statistics for Engineers (PSE) (Th + Tut):** Ruhi Mavalankar (Math Dept.)
Minor (Microcontrollers - Offered by E & TC dept.): Mr. P. P. Tasgaonkar, **Honors (Random Signals and Stochastic Processes):** Mrs. S. M. Tamsekar
IFC - Data Analytics (DA)(Th + Lab): (Comp Dept.) , **Constitution of India (COI)(Th):**
Entrepreneurship Principles and Process (EPP)(Th):

IFC - Internet of Things and Applications (offered by E &TC to Civil): Dr. Ms. D. V. Niture and **(Meta):** Mr. P. P. Tasgaonkar

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In-charge, Timetable Committee,
Department of Electronics & Telecommunication Engineering