

Department of Electronics and Telecommunication Engineering

**COEP Technological University**

(A Unitary Public University of Government of Maharashtra)

**Time Table**

Class : S. Y. B. Tech E & TC

With Effect From : 17.08.2023

Academic Year : 2023 - 2024

Term : I (First)

| Day | 9.00 am<br>To<br>10.00 am                   | 10.00 am<br>To<br>11.00 am                    | 11.00 am<br>To<br>12.00 pm     | 12.00 pm<br>To<br>1.00 pm                          | 1.00 pm<br>To<br>2.00 pm | 2.00 pm<br>To<br>3.00 pm  | 3.00 pm<br>To<br>4.00 pm                       | 4.00 pm<br>To<br>5.00 pm | 5.00 pm<br>To<br>6.00pm       |
|-----|---|---|--------------------------------|--|--------------------------|---------------------------|--|--------------------------|-------------------------------|
| MON | LA & UC –<br>DSY<br>(AC-201)<br>(8-10am)    | S&S Tut (A)<br>(SPMe)                         | DSD<br>(VSA)<br>(ET-001)       | EDC<br>(SPM)<br>(ET-001)                           | Lunch Break              | NSAF<br>(YDK)<br>(ET-001) | EDC (A), DSD (B), CS (C),<br>S&A (D), NSAF (E) |                          | PLEVAH<br>(ETSH-201)<br>(SSD) |
| TUE | LA & UC –<br>DSY<br>(AC-201)<br>(8.30-10am) | FP – DSY<br>(AC-101)<br>S&S Tut (B)<br>(SPMe) | ODE & MVC<br>(ABD)<br>(ET-001) | EDC<br>(SPM)<br>(ET-001)                           |                          | NSAF<br>(YDK)<br>(ET-001) | EDC (B), DSD (C), CS (D),<br>S&A (E), NSAF (A) |                          |                               |
| WED | LA & UC –<br>DSY<br>(AC-201)<br>(8.30-10am) | S&S Tut (C)<br>(SPMe)                         | ODE & MVC<br>(ABD)<br>(ET-001) | I&C<br>( )   |                          | S&S<br>(SPMe)<br>(ET-001) | EDC (C), DSD (D), CS (E),<br>S&A (A), NSAF (B) |                          |                               |
| THU | FP – DSY<br>(AC-201)                        | S&S Tut (D)<br>(SPMe)                         | S&S<br>(SPMe)<br>(ET-001)      | EDC<br>(SPM)<br>(ET-001)                           |                          | DSD<br>(VSA)<br>(ET-001)  | EDC (D), DSD (E), CS (A),<br>S&A (B), NSAF (C) |                          |                               |
| FRI | FP – DSY<br>(AC-101)                        | S&A-IFC<br>(MAK)<br>(ET-001)                  | DSD<br>(VSA)<br>(ET-001)       | ODE &<br>MVC-TUT<br>(ABD)<br>S&S Tut (E)<br>(SPMe) |                          | NSAF<br>(YDK)<br>(ET-001) | EDC (E), DSD (A), CS (B),<br>S&A (C), NSAF (D) |                          |                               |

**Electronic Devices & Circuits (EDC) (Th + Lab: Batches-B&C):** Dr. S. P. Mahajan, **(EDC) (Lab: Batches-A, D & E):** Dr. Saiyma Fatima Raza

**Digital System Design (DSD) (Th + Lab):** Dr. Mrs. V. S. Agarwal, **Signals & Systems (S&S) (Th + Tut):** Dr. Ms. S. P. Metkar,

**Network Synthesis and Analog Filter (NSAF) (Th +Lab):** Mrs. Y. D. Kapse, **Circuit Simulation (Lab):** Dr. Ms. S. P. Metkar,

**Sensors and Automation (S&A)(Th + Lab):** Mrs. M. A. Khandekar (Instrumentation Dept.),

**ODE & MVC (Th + Tut):** Mrs. A. B. Dhere (Mathematics Dept.), **Innovation and Creativity (I&C):**

**Professional Laws, Ethics, Values and Harmony (PLEVH)(TH):**

**For DSY: LA & UC (Th):** (Mathematics Dept.), **Foundation of Physics (FP)(TH):** (Applied Science Dept.)

In-charge, Timetable Committee,  
Department of Electronics & Telecommunication Engineering