

Department of Electronics and Telecommunication Engineering

COEP Technological University

(A Unitary Public University of Government of Maharashtra)

Time Table

Class : M. Tech (Electronics - VLSI and Embedded Systems)

With Effect From : 04.01.2024

Academic Year : 2023 - 2024

Term : II (Second)

Day TIME	9.00 am To 10.00 am	10.00 am To 11.00 am	11.00 am To 12.00 pm	12.00 pm To 1.00 pm	1.00 pm To 2.00 pm	2.00 pm To 3.00 pm	3.00 pm To 4.00 pm	4.00 pm To 5.00 pm	5.00 pm To 6.00 pm
MON		DCMOS Lab (SFR)		IoT (VSV) ETSH-201	Lunch Break	VLSI (VVI) ETSH-201	VLSI (VVI) ETSH-201		
TUE		ACMOS (VSA) ETSH-201	ACMOS (VSA) ETSH-201	IoT (VSV) ETSH-201		VLSI Lab (VVI)			
WED	OE	DCMOS (SFR) ETSH-201	LES (PPB) ETSH-201	IoT (VSV) ETSH-201		LES Lab (PPB)			
THU	OE	DCMOS (SFR) ETSH-201	LES (PPB) ETSH-201	VLSI Tut (VVI) ETSH-201					LLC
FRI	OE	DCMOS (SFR) ETSH-201	LES (PPB) ETSH-201	ACMOS (VSA) ETSH-201					LLC
SAT									

Linux in Embedded Systems (LES) (Th + Lab): Dr. Mr. P. P. Bartakke, **Digital CMOS VLSI Design (DCMOS) (Th + Lab):** Dr. (Mrs.) Saiyma F. Raza
VLSI Design Verification and Testing (VLSI) (Th + Lab): Dr. Mrs. V. V. Ingale

Department Elective:

DE-II Internet of Things (IoT) (Th): Dr. (Mrs.) V. S. Vyas, **DE-III Analog CMOS VLSI Design (ACMOS)(Th):** Dr. (Mrs.) V. S. Agarwal

Liberal Learning Course (LLC): **Open Elective (OE):**

Timetable In-charge,
Department of Electronics & Telecommunication Engineering