

**DEPARTMENT OF ELECTRICAL ENGINEERING**  
**COLLEGE OF ENGINEERING, PUNE**  
**Time – Table**

**Academic Year – 2017-18**  
**Term – Second**  
**Class Room:- EE 101**

**Class- S. Y. B. Tech.**  
**With effect from – 16/1/2018**

TIME	08.00	09.00	10.00	11.00	12.00	01.00	02.00	03.00	04.00	05.00
	AM TO 09.00 AM	AM TO 10.00 AM	AM TO 11.00 AM	AM TO 12.00 PM	PM TO 01.00 PM	PM TO 02.00 PM	PM TO 03.00 PM	PM TO 04.00 PM	PM TO 05.00 PM	PM TO 06.00 PM
PERIOD	1	2	3	4	5	6	7	8	9	10
MON		Direct SY Maths AC 203	T&DCM	VC & PDE		DS & CP Lab B T&DCM Lab C DE Lab D  EMF Tut A (2-3) (EE 101)		S of LS	T&DCM Tut D (E Mc Lab)	
TUE		Direct SY Maths AC 203	T&DCM	DE		DS & CP Lab C T&DCM Lab D DE Lab A  EMF Tut B (2-3) (EE 101)		S of LS	DS & CP	
WED		Direct SY Maths AC 203 T&DCM Tut A (Mc Lab)	ILOE	VC & PDE		DS & CP Lab D T&DCM Lab A DE Lab B T&DCM Tut C (1-2) (EE 101) EMF Tut C (2-3) (EE 101)		VC & PDE Tut (EE 101, 102, 103)	EMF	
THR		Direct SY Maths AC 203 T&DCM Tut B (MC Lab)	ILOE	EMF		DS & CP Lab A T&DCM Lab B DE Lab C  EMF Tut D (2-3) (EE 101)		DE		
FRI		Direct SY Maths AC 203	ILOE	DE		T&DCM	EMF	S of LS		
SAT										

**Subjects :**

Transformer and DC Machines: Prof. R. T. Ugale  
 Digital Electronics (DE): Prof. Shreyas Deshmukh  
 Electro Magnetic Fields (EMF): Prof. A. P. Deshpande  
 Electro Magnetic Fields Tutorial (EMF TUT) : Prof. A. P. Deshpande  
 Transformer and DC Machines Tutorial (E M/C I TUT): Prof. V. B. Bhole  
 Digital Electronics Lab : Prof. Prashant Gawande  
 Transformer and DC Machines Lab: Prof. R. T. Ugale & Prof. B. N. Chaudhari  
 Data Structure & Computer Programming Lab: Prof. Rohan Kulkarni and Prof. Trupti Hinge  
 Data Structure & Computer Programming (DS & CP) : Prof. Rohan Kulkarni

**College of Engineering Pune**  
**Electrical Engineering**  
**Timetable For class: FYECS**

Academic Year –2017-18

Term – First

With effect from – 28 August 2017

	08:00	09:00	10:00	11:00	12:00	13:00	14:00	15:00	16:00	17:00	18:00
Mon				ECS ECSSL SRK COM 1			ALCS_TH SRK EE103	DCS_TH VSR EE102	ILOE		
Tue				ECS ECSSL SRK COM 1			DCS_TH VSR EE102	DCS_TH VSR EE101	ILOE		
Wed			MDS_TH DBT EE101	ALCS_TH SRK EE103	ALCS_TH SRK EE103		MDS_THS SVJ EE103	MDS_TH SVJ EE103	ILOE		
Thu			MDS_TH DBT EE101			Humanities EE 104			ECS ES I_L BRT COM 1		
Fri			DCS_Tut VSR EE103	MDS_Tut DBT EE104			ECS ES I_L BRT COM 2			RM KAM EE101	
Sat			MDS_TH DBT EE101	MDS_TH DBT EE101							

DCS\_TH (Digital Control System): Dr. V S Rajguru

MDS\_Th (Modeling of Dynamic Systems): Dr. D B Talange

ALCS\_TH (Advanced Linear Control Systems): Dr S R Kurode

RM (Research Methodology): Prof. Kamat

ECSSL (Simulation Lab): Dr S R Kurode

ES I\_L (Embedded System I Lab): Prof. Bhagyashri Tamhane & Dr. D. B Talange

**DEPARTMENT OF ELECTRICAL ENGINEERING  
COLLEGE OF ENGINEERING, PUNE  
Time – Table**

Academic Year – 2017-18  
Term – Second

Class - B.Tech.  
With effect from – 16/1/2018

TIME	08.00 AM TO 09.00 AM	09.00 AM TO 10.00 AM	10.00 AM TO 11.00 AM	11.00 AM TO 12.00 PM	12.00 PM TO 01.00 PM	01.00 PM TO 02.00 PM	02.00 PM TO 03.00 PM	03.00 PM TO 04.00 PM	04.00 PM TO 05.00 PM	05.00 PM TO 06.00 PM
	1	2	3	4	5	6	7	8	9	10
MON		E&HEV (EE 103)					ICS EE 103	ICS EE 103	HVDC & FACTS (EE 104)	
TUE		E&HEV (EE 103)		AED (EE 102)	EA&C (EE 104)					
WED		ILOE		AED (EE 102)	EA&C (EE 104)			ICS (EE 103)		
THR		ILOE			EA&C (EE 104)		HVDC & FACTS (EE 104)			
FRI		ILOE	HVDC & FACTS (EE 103)	IPR (EE 104)			E&HEV (EE 103)	AED (EE 103)		
SAT										

Subjects :

Energy Auditing and Conservation (EA&C): Prof. Mrs. A. A. Dharme

HVDC & FACTS: Prof. Mrs. M. Murali

Intelligent Control : Prof. Mrs. S. V. Jadhav

Advanced Electric Drives : Prof. R. T. Ugale and Prof. B N Chaudha

Electric & Hybrid Electric Vehicles: Prof. B N Chaudhari

**College of Engineering Pune**  
**Electrical Engineering**  
**Timetable For class: BTECH**

Academic Year –2017-18

Term – First

With effect from – 7 August 2017

	08:00	09:00	10:00	11:00	12:00	13:00	14:00	15:00	16:00	17:00
<b>Mon</b>			EMD(DE-I) RTU EE 101	ICS(DE-II) BRT EE103		B Tech A ED(DE-III)L MB EM_L		DCS_TH VSR EE103		
<b>Tue</b>				WSP_TH UMM EE 101		B Tech B ED(DE-III)L MB EM_L		DCS_TH VSR EE103	WSP_TH UMM EE 104	
						DCS_TH VSR EE103				
<b>Wed</b>		ILOE	ICS(DE-II) BRT EE103	HV(DE-I) AAD Seminar Hall EMD(DE-I) RTU Measurement Lab		B Tech C ED(DE-III)L MB EM_L		WSP_TH UMM EE 104	ICS(DE-II) BRT EE104	
<b>Thu</b>		ILOE		HV(DE-I) AAD Seminar Hall EMD(DE-I) RTU EE101		B Tech D ED(DE-III)L MB EM_L		B Tech B HV(DE-I)L AAD HV LAB		
<b>Fri</b>		ILOE	DCS_TH VSR EE103			ED(DE-III) SSD EE104	ED(DE-III) SSD EE104	B Tech A HV(DE-I)L AAD HV LAB		
<b>Sat</b>										

Wind and Solar Power (WSP\_TH): Dr. U. Mate

High Voltage Lab (HV): Dr. A. Dharme

Digital Control Systems (DCS): Dr. V. Rajguru

Electric Machine Design (EMD): Dr. R. Ugale

Industrial control System (ICS): Prof. B. Tamhane  
Electric Drives (ED): Prof. S. Dambhare and Mr. Bagewadi

**DEPARTMENT OF ELECTRICAL ENGINEERING  
COLLEGE OF ENGINEERING, PUNE  
Time – Table**

Academic Year – 2017-18  
Term – Second

Class - M. Tech. ECS  
With effect from – 16/1/2018

TIME	08.00 AM TO 09.00 AM	09.00 AM TO 10.00 AM	10.00 AM TO 11.00 AM	11.00 AM TO 12.00 PM	12.00 PM TO 01.00 PM	01.00 PM TO 02.00 PM	02.00 PM TO 03.00 PM	03.00 PM TO 04.00 PM	04.00 PM TO 05.00 PM	05.00 PM TO 06.00 PM
	1	2	3	4	5	6	7	8	9	10
MON		E&HEV (EE 103)	ESD (Seminar hall)	ESD (Seminar hall)		CRS EE 103	ICS EE 103	ICS EE 103		
TUE		E&HEV (EE 103)	SMC (EE 102)	ESD (Seminar hall)		SMC EE 103	NLDS EE 103	ESS (103)		
WED		SMC (EE 102)	NLDS EE 102	MCPC PE Lab	MCPC PE Lab		CRE EE 103	ICS EE 104		
THR		SMC Tut (EE 102)	Emb Lab 2				MCPC EE 104	ESS (103)	IPR EE 101	
FRI		ESS (103)	Emb Lab 2				E&HEV (EE 103)	NLDS EE 102	CRE EE 102	
SAT										

**Subjects :**

Sliding Mode Control (SMC): Prof. Mrs. S. R. Kurode

Nonlinear Dynamical Systems (NLDS) : Prof. Mrs. S. V. Jadhav

Embedded System Design (EMD): Prof. Joglekar

Embedded System Lab II : Prof. Rohan Kulkarni and Prof. S R Kurode

Energy Storage Systems : Prof. D. B. Talange

Electric and Hybrid Electric Vehicles (E&HEV) : Prof. B. N. Chaudhari

Control Related Estimations: Prof. Bhagyashri Tamhane

**College of Engineering Pune**  
**Electrical Engineering**  
**Timetable For class: FYECS**

Academic Year –2017-18

Term – First

With effect from – 28 August 2017

	08:00	09:00	10:00	11:00	12:00	13:00	14:00	15:00	16:00	17:00	18:00
Mon				ECS ECSSL SRK COM 1			ALCS_TH SRK EE103	DCS_TH VSR EE102	ILOE		
Tue				ECS ECSSL SRK COM 1			DCS_TH VSR EE102	DCS_TH VSR EE101	ILOE		
Wed			MDS_TH DBT EE101	ALCS_TH SRK EE103	ALCS_TH SRK EE103		MDS_THS SVJ EE103	MDS_TH SVJ EE103	ILOE		
Thu			MDS_TH DBT EE101			Humanities EE 104			ECS ES I_L BRT COM 1		
Fri			DCS_Tut VSR EE103	MDS_Tut DBT EE104			ECS ES I_L BRT COM 2			RM KAM EE101	
Sat			MDS_TH DBT EE101	MDS_TH DBT EE101							

DCS\_TH (Digital Control System): Dr. V S Rajguru

MDS\_Th (Modeling of Dynamic Systems): Dr. D B Talange

ALCS\_TH (Advanced Linear Control Systems): Dr S R Kurode

RM (Research Methodology): Prof. Kamat

ECSSL (Simulation Lab): Dr S R Kurode

ES I\_L (Embedded System I Lab): Prof. Bhagyashri Tamhane & Dr. D. B Talange

**DEPARTMENT OF ELECTRICAL ENGINEERING**  
**COLLEGE OF ENGINEERING, PUNE**  
**Time – Table**

Academic Year – 2017-18  
 Term – Second

Class - M. Tech. PEMD  
 With effect from – 16/1/2018

TIME	08.00 AM TO 09.00 AM	09.00 AM TO 10.00 AM	10.00 AM TO 11.00 AM	11.00 AM TO 12.00 PM	12.00 PM TO 01.00 PM	01.00 PM TO 02.00 PM	02.00 PM TO 03.00 PM	03.00 PM TO 04.00 PM	04.00 PM TO 05.00 PM	05.00 PM TO 06.00 PM
	1	2	3	4	5	6	7	8	9	10
MON		E&HEV (EE 103)	PEAPS (EE 103)	DSP App to PE & D (EE 102)		DPES EE 102	DPES (EE 102)	Wind & Solar (EE 102)		PEAES tut (EE 103)
TUE		E&HEV (EE 103)	PEAPS (EE 103)	AED (EE 102)		DPES (EE 102)	Wind & Solar EE 102	ESS (103)		
WED		DSP App to PE & D (EE 103)	PEAPS (EE 103)	AED (EE 102)			Wind & Solar (EE 102)			
THR			Emb Lab 2				DSP App to PE & D (EE 102)	ESS (103)	IPR EE 101	
FRI		ESS (103)	Emb Lab 2				E&HEV (EE 103)	AED (EE 102)		
SAT										

**Subjects :**

Advanced Electric Drives (AED) : Prof. R. T. Ugale

Power Electronics applications to Energy Systems (PEAES): Prof. M. Murali

DSP applications to Power Electronics and Drives : Prof. Rohan Kulkarni

Wind & Solar : Prof. U M Mate

Electric and Hybrid Electric Vehicles (E&HEV) : Prof. B. N. Chaudhari

Embedded System Lab II : Prof. Rohan Kulkarni and Prof. S R Kurode

Energy Storage Systems: Prof. D B Talange



**College of Engineering Pune**  
**Electrical Engineering**  
**Timetable For class: FYPEMD**

Academic Year –2017-18

Term – First

With effect from – 28 August 2017

	08:00	09:00	10:00	11:00	12:00	13:00	14:00	15:00	16:00	17:00	18:00
Mon			PEMD APE_L VSR EEM_L/COM2				AEM_TH RTU EE102		ILOE		
Tue			ES_TH SJ EE 102	AEM_TH RTU EE102					ILOE		
Wed			PEMD APE_L VSR EEM_L/COM2					APE_TH VSR EE102	ILOE		
Thu			ES_TH SJ EE 102	ES_TH SJ EE 102		Humanities EE 104	APE_TH VSR EE102	PEMD ES I_L RK COM 2			
Fri		PEMD ES I_L RK COM 1					AEM_TH RTU EE102	APE_TH VSR EE102	AEM_Tut RTU EE102	RM KAM EE101	
Sat						APE_Tut VSR EE102	APE_Tut VSR EE102				

APE\_Th (Advanced Power Electronics Theory) : Dr. V S Rajguru

APE\_L (Advanced Power Electronics Lab) : Dr. V S Rajguru

ES\_TH (Embedded System Theory): Prof S Joglekar

RM (Research Methodology): Prof. Kamat

AEM\_TH (Analysis of Electrical Machines): Dr. R T Ugale

ES I\_L (Embedded System I Lab): Prof. Rohan Kulkarni

**DEPARTMENT OF ELECTRICAL ENGINEERING  
COLLEGE OF ENGINEERING, PUNE  
Time – Table**

Academic Year – 2017-18  
Term – Second

Class- M. Tech. PEPS  
With effect from – 16/1/2018

TIME	08.00 AM	09.00 AM	10.00 AM	11.00 AM	12.00 PM	01.00 PM	02.00 PM	03.00 PM	04.00 PM	05.00 PM
	TO 09.00 AM	TO 10.00 AM	TO 11.00 AM	TO 12.00 PM	TO 01.00 PM	TO 02.00 PM	TO 03.00 PM	TO 04.00 PM	TO 05.00 PM	TO 06.00 PM
PERIOD	1	2	3	4	5	6	7	8	9	10
MON		E&HEV (EE 103)	PEAPS (EE 103)	PSDS (EE 103)		Hardware Lab		EPDS (EE 104)		PEAPS Tut (EE 103)
TUE		E&HEV (EE 103)	PEAPS (EE 103)	PSDS (EE 103)		Hardware Lab		ESS (103)		
WED			PEAPS (EE 103)	DP (EE 103)		Hardware Lab		PST (Seminar Hall)		
THR			EPDS (EE 103)	PSDS (EE 103)		PST (EE 103)	PST (EE 103)	ESS (103)	IPR EE 101	
FRI		ESS (EE 103)	DP (EE 103)	DP (EE103)		EPDS (EE 103)	E&HEV (EE 103)			
SAT										

**Subjects :**

**Power System Dynamics and Stability (PSDS):**Prof. S. S. Dambhare

**Power Electronics Application to Power Systems (PEAPS) :** Prof. M. Murali

**Digital Protection (DP) :** Prof. Mrs. S. V. Unde

**Electrical Power Distribution Systems (EPDS) :** Prof. Mrs. S. P. Ghanegaokar

**Electrical and Hybrid Electric Vehicles (E&HEV) :** Prof. B. N. Chaudhari

**Hardware Laboratory :** Prof. Meera Murali

**Power System Transients:** Prof. S S Dambhare and Prof. S V Unde

**College of Engineering Pune**  
**Electrical Engineering**  
**Timetable For class: FYPEPS**

Academic Year –2017-18

Term – First

With effect from – 28 August 2017

	08:00	09:00	10:00	11:00	12:00	13:00	14:00	15:00	16:00	17:00	18:00
Mon				PSA(PG) VNP EE102		EO(PG) SPG EE102	AEM_TH RTU EE102		ILOE		
Tue				AEM_TH RTU EE102		EO(PG) SPG EE102	PSA(PG) VNP EE102		ILOE		
Wed			PEPS SL VNP COM 1				EO(PG) SPG EE102	APE_TH VSR EE102	ILOE		
Thu		PEPS SL VNP COM 1				Humanities EE 104	APE_TH VSR EE102				
Fri					PSA(PG) VNP EE102		AEM_TH RTU EE102	APE_TH VSR EE102	AEM_Tut RTU EE102	RM KAM EE101	
Sat							APE_Tut VSR EE102	APE_Tut VSR EE102			

EO (Engg. Optimization): Dr. S P Ghanegaonkar

APE\_Th (Advanced Power Electronics Theory) : Dr. V S Rajguru

SL (Simulation Lab): Dr. V. N Pande

PSA (Power System Analysis): Dr. V. N. Pande

RM (Research Methodology): Prof. Kamat

AEM\_TH (Analysis of Electrical Machines): Dr. R T Ugale

**College of Engineering Pune**  
**Electrical Engineering**  
**Timetable For class: SYEE**

Academic Year –2017-18  
 Term – First

	09:00	10:00	11:00	12:00	13:00	14:00	15:00	16:00	17:00
<b>Mon</b>	LA&UC	FP	SDL_TH SDD EE101	Lunch	SY A/SDLC_L SDL_L/PK SY B/COM 2 CSL/PG SY C/COM 1 NMCP_L/MM SY D/EEM_L EEM_L/APD		EEM_TH APD EE101	PE Tut Batch C 101	
<b>Tue</b>	LA&UC	FP	SY D/SDLC_L SDL_Tut/SDD SY A/EEM_L ECA_Tut/VBB SY C/104 PE Tut B		SY B/SDLC_L SDL_L/PK SY C/COM 2 CSL/PG SY D/COM 1 NMCP_L/MM SY A/EEM_L EEM_L/APD		EEM_TH APD EE101	ECA_TH VBB EE101	NMCP_TH RK EE104
<b>Wed</b>	LA&UC	FP	ODE_TH MF1 EE101		SY C/SDLC_L SDL_L/PK SY D/COM 2 CSL/PG SY A/COM 1 NMCP_L/MM SY B/EEM_L EEM_L/APD		ECA_TH VBB EE101	PE Tut Batch A 102	
<b>Thu</b>	LA&UC	SY C/SDLC_L SDL_Tut/SD D SY D/EEM_L ECA_Tut/VB B	ODE_TH MF1 EE103		SY D/SDLC_L SDL_L/PK SY A/COM 2 CSL/PG SY B/COM 1 NMCP_L/MM SY C/EEM_L EEM_L/APD		SDL_TH SDD EE101	SY A/SDLC_L SDL_Tut/SDD SY B/EEM_L ECA_Tut/VBB	
<b>Fri</b>	LA&UC	ECA_TH VBB EE101	SY Batch1/SDLC_L ODE_Tut/MF1 SY Batch2/EE104 ODE_Tut/MF3 SY Batch3/PS_L ODE_Tut/MF2		SDL_TH SDD EE101	EEM_TH APD EE101	NMCP_TH RK EE101	SY B/SDLC_L SDL_Tut/SDD SY C/EEM_L ECA_Tut/VBB	Innovation
<b>Sat</b>									

Solid State Devices and Linear Circuits (SDLC): Prof. Shreyas Deshmukh  
 Electrical Circuit Analysis (ECA): Prof. V. Bhole  
 Electrical and Electronic Measurements (EEM): Dr. A. P. Deshpande  
 Solid State Devices and Linear Circuits Lab(SDLC\_L): Prof. Pratap Koppollu  
 Numerical Methods and Computer Programming Lab (NMCP\_L): Dr. M. Murali  
 Numerical Methods and Computer Programming (NMCP\_TH): Prof. Rohan Kulkarni  
 Measurement Lab (EEM\_L): Dr. A. P. Deshpande

Circuit Simulation Lab (CS\_L): Mr. Prashant Gawande  
Solid State Devices and Linear Circuits Tut (SDLC\_Tut): Prof. Shreyas Deshmukh  
Professional Ethics (PE Tutorial): Prof Kher

**DEPARTMENT OF ELECTRICAL ENGINEERING**  
**COLLEGE OF ENGINEERING, PUNE**  
**Time – Table**

Academic Year – 2017-18

Term – Second

Class Room:- EE 104

Class- T. Y. B.Tech.

With effect from – 16/1/2018

TIME	08.00	09.00	10.00	11.00	12.00	01.00	02.00	03.00	04.00	05.00
	AM TO 09.00 AM	AM TO 10.00 AM	AM TO 11.00 AM	AM TO 12.00 PM	PM TO 01.00 PM	PM TO 02.00 PM	PM TO 03.00 PM	PM TO 04.00 PM	PM TO 05.00 PM	PM TO 06.00 PM
PERIOD	1	2	3	4	5	6	7	8	9	10
MON			Honors ESD (Seminar Hall)	Honors ESD (Seminar Hall) CS Tut D (EE 104)		CS1	PSOC	CS I Lab-A PSOCLab-B PE Lab-C MP in DSP Lab-D		Minor
TUE			PE	Honors ESD (Seminar Hall)		PE Tut A (PE lab) CS Tut B (EE 104)	PSOC	CS I Lab-B PSOCLab-C PE Lab-D MP in DSP Lab-A		Minor
WED			PE	CS1		PE Tut B (PE lab)	PSOC	CS I Lab-C PSOCLab-D PE Lab-A MP in DSP Lab-B		Minor
THR			PE	PSOC		DSP	PE Tut C (PE lab)	CS Tut C (EE 104)	ILOE in Humanities	
FRI			Environmental Studies	CS1		PE Tut D (PE lab) CS Tut A (EE 104)	CS I Lab-D PSOCLab-A PE Lab-B MP in DSP Lab-C		ILOE in Humanities	
SAT										

**Subjects :**

Control Systems-I : Prof. S. R. Kurode

Power System Operaton and Control : Prof. A. A. Dharme

Power Electronics : Prof. D. B. Talange

Control System Tutorial: Prof. V. B. Bhole

Power Electronics Tutorial : Prof. Pratap Kumar

Power System Operaton and Control Lab : Prof. S. S. Damhare and Prof. S. V. Unde

Control Systems Lab- I : Prof. S. R. Kurode and Prof. Bhagyashri Tamhane

Power Electronics Lab: Prof. V. S. Rajguru

Mini Project in DSP Lab: Prof. Shreyas Deshmukh, Prof. R Kulkarni, Prof. D B Talange

**College of Engineering Pune**  
**Electrical Engineering**  
**Timetable For class: TYEE**

Academic Year –2017-18

Term – First

With effect from – 19/07/2017

	09:00	10:00	11:00	12:00	13:00	14:00	15:00	16:00	17:00
Mon		PSA(UG) TPH EE104	Mathematics MF1 EE104		Slot for Honors EO EE102	MIC_TH SDD EE104	TY A/COM 1 PSA_L/TPH TY B/EM_L ACM_L/PB TY D/MIC_L MIC_L/UMM		MINOR_V
Tue	ACM_TH BNC EE101	SP_TH RK EE104	TY A/MIC_L SP_Tut/RK TY B/103 ACM_Tut/PB		Slot for Honors EO EE102	MIC_TH SDD EE104	TY B/COM 1 PSA_L/TPH TY A/MIC_L MIC_L/SDD TY C/EM_L ACM_L/RTU		MINOR_V
Wed	ACM_TH BNC EE101	MIC_TH SDD EE104	PSA(UG) TPH EE104		TY C/MIC_L SP_Tut/RK TY D/103 ACM_Tut/PB	Slot for Honors EO EE 102	TY C/COM 1 PSA_L/TPH TY D/EM_L ACM_L/RTU TY B/MIC_L MIC_L/SDD		MINOR_V
Thu	ACM_TH BNC EE101	SP_TH RK EE104	Mathematics MF1 EE104		SP_TH RK EE104	PSA(UG) TPH EE104	PSA(UG) TPH EE104	Humanities	
Fri	C of I EE 104	Entrepreneurship EE 104	Mathematics MF1 EE103		TY D/MIC_L SP_Tut/RK TY A/103 ACM_Tut/PB	TY D/COM 1 PSA_L/TPH TY A/EM_L ACM_L/PB TY C/MIC_L MIC_L/SDD		Humanities	TY B/MIC_L SP_Tut/RK TY C/103 ACM_Tut/ PB
Sat									

Power Systems Analysis (PSA\_TH) : Prof. Mrs. Trupti Hinge  
 Microcontroller and applications (MIC\_TH): Prof. Shreyas Deshmukh  
 Signal Processing (SP\_TH) : Prof. Rohan Kulkarni  
 AC Machines Lab (ACM\_L): Dr. B. N. Chaudhari  
 AC Machines Tutorial (ACM\_TUT): Mrs. Priyanka Bastawade  
 Power Systems Analysis Lab (PSA\_L) : Prof. Mrs. Trupti Hinge  
 Microcontroller Lab (MIC\_L): PROF.Mrs.U.M.Mate and Prof. Shreyas Deshmukh  
 Signal Processing Tutorial (SP\_TUT) : Prof. Rohan Kulkarni  
 Constitution of India (C of I): Prof. S Deshpande  
 Mathematics: Prof. More

